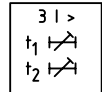
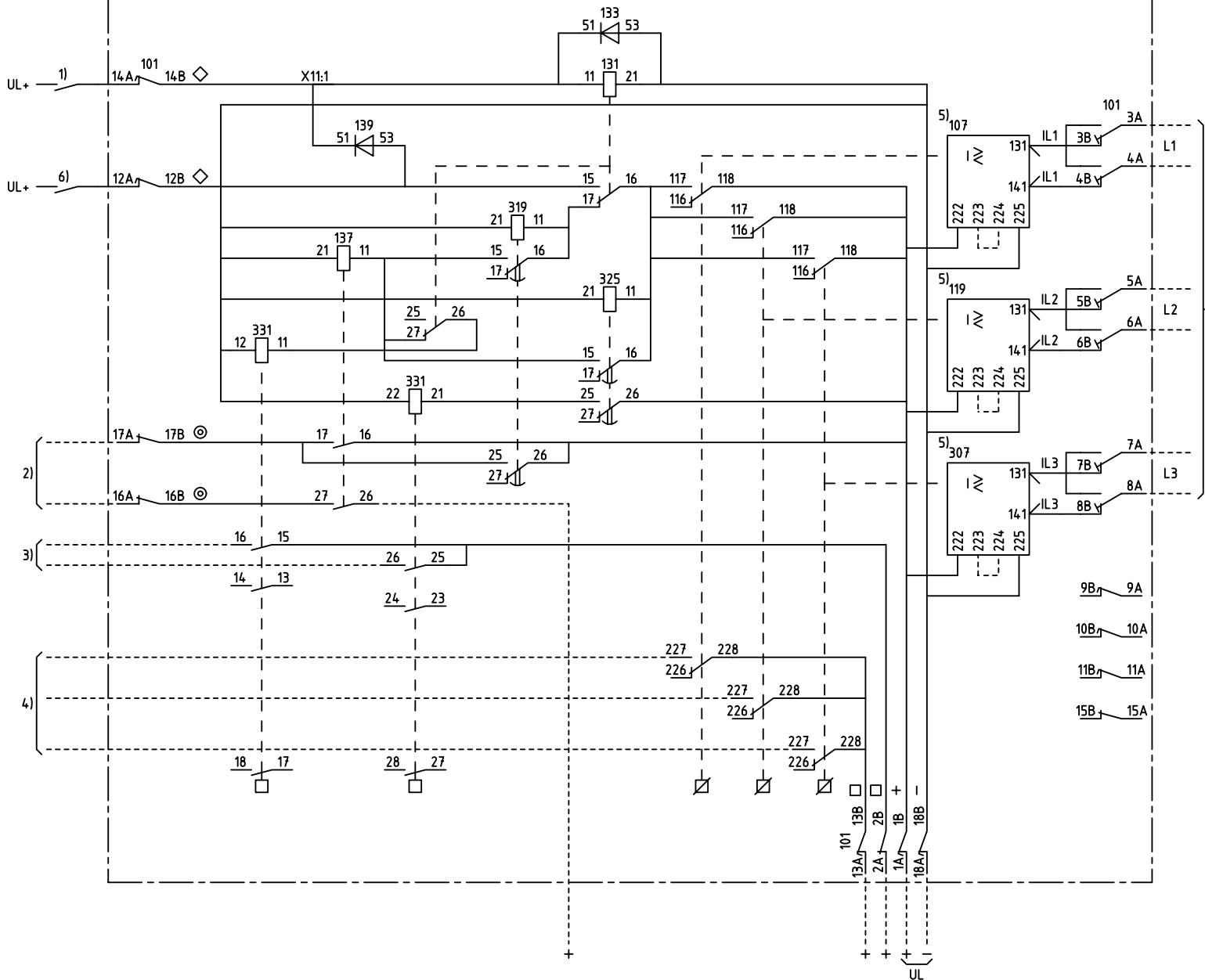


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3-PHASE OVERCURRENT PROTECTION WITH TWO TIME STEPS. FIRST STEP CAN BE BLOCKED $t_1 < t_2$
 3-FAS ÖVERSTRÖMSSKYDD MED TVÅ TIDSTEG. FÖRSTA STEGET KAN BLOCKERAS $t_1 < t_2$

42C				
101	107	119	131	137
			133	139
307	319	325	331	

4U

- 101 RTXP 18
- 107 119 307 RXIG 21
- 131 RXMA 1
- 137 RXME 1
- 319 325 RXKL 1 t_1, t_2
- 331 RXSF 1
- 133 139 RTXE

- 1) EXTERNAL BLOCKING
EXTERN BLOCKERING
- 2) TRIPPING ETC.
UTLÖSNING M.M.
- 3) ALARM ETC.
SIGNAL M.M.
- 4) INDICATING ETC.
INDIKERING M.M.
- 5) 223-224 CONNECTED AT UL
—24 RESP 110-125V
223-224 FÖRBUNDNA VID UL
—24 RESP 110-125V
- 6) START OF BACK-UP STEP, WHEN THE SHORT-CIRCUIT POWER IS TOO LOW
START AV RESERVSTEG, NÄR KORTSLUTNINGSEFFEKTEN ÄR FÖR LÅG

ELCAD ID PAT/render DATE 2004-06-2221:19

RK 651 030-BB Issued in ARCADE

Prepared 84 05 C-H EINVALL		CIRCUIT DIAGRAM/KRETSSCHEMA	
Approved 84 05 P JENÄKER		3-PHASE OVERCURRENT PROTECTION	
		3-FAS ÖVERSTRÖMSSKYDD	
Rev Ind	Revision	Reg No 7431	Resp dep PAT
Based on	Pcl 890.651		Rev Ind 6 96 45
		Lang 82	
		Sheet 1	
		Cont	

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