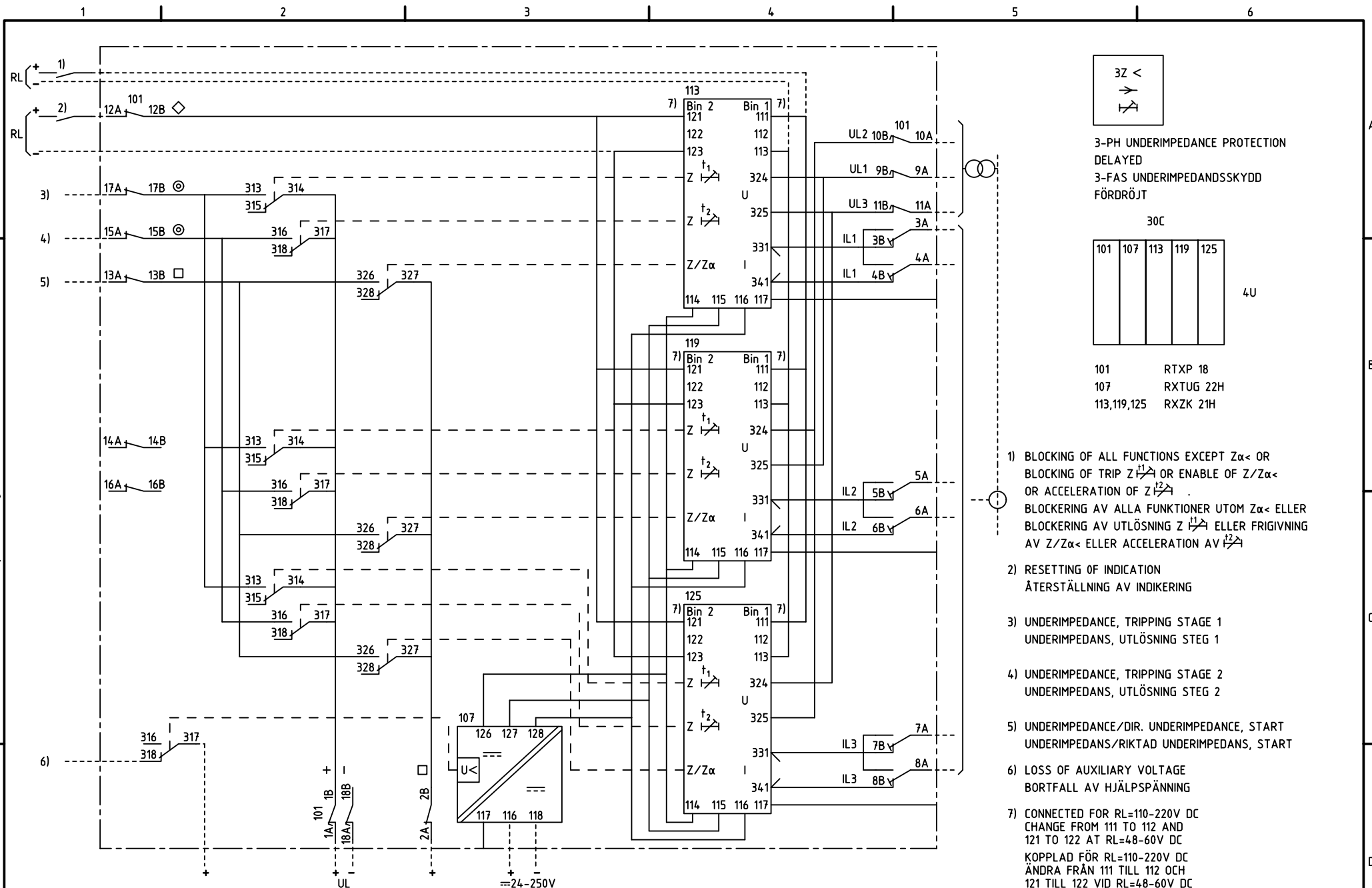


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- 1) BLOCKING OF ALL FUNCTIONS EXCEPT  $Z_{\alpha <}$  OR BLOCKING OF TRIP  $Z t_1$  OR ENABLE OF  $Z/Z_{\alpha <}$  OR ACCELERATION OF  $Z t_2$ .  
BLOCKERING AV ALLA FUNKTIONER UTOM  $Z_{\alpha <}$  ELLER BLOCKERING AV UTLÖSNING  $Z t_1$  ELLER FRIGIVNING AV  $Z/Z_{\alpha <}$  ELLER ACCELERATION AV  $Z t_2$
- 2) RESETTING OF INDICATION  
ÅTERSTÄLLNING AV INDIKERING
- 3) UNDERIMPEDANCE, TRIPPING STAGE 1  
UNDERIMPEDANS, UTLÖSNING STEG 1
- 4) UNDERIMPEDANCE, TRIPPING STAGE 2  
UNDERIMPEDANS, UTLÖSNING STEG 2
- 5) UNDERIMPEDANCE/DIR. UNDERIMPEDANCE, START  
UNDERIMPEDANS/RIKTAD UNDERIMPEDANS, START
- 6) LOSS OF AUXILIARY VOLTAGE  
BORTFALL AV HJÄLPSPÄNNING
- 7) CONNECTED FOR  $RL=110-220V$  DC CHANGE FROM 111 TO 112 AND 121 TO 122 AT  $RL=48-60V$  DC  
KOPPLAD FÖR  $RL=110-220V$  DC ÄNDRA FRÅN 111 TILL 112 OCH 121 TILL 122 VID  $RL=48-60V$  DC

ELCAD ID PAT/render		DATE 2004-06-2223:59		IMRK 001 029-PA		Issued in ARCADE	
		Prepared 96-06-13	H Raza/LB		CIRCUIT DIAGRAM/KRETSSCHEMA		
		Approved 96-08-19	P Jenåker		RAZK 213 3-PHASE UNDERIMPEDANCE PROT.		
					RAZK 213 3-FAS UNDERIMPEDANSSKYDD		
Rev Ind	Revision	Reg No 7411			Resp dep PAT	Rev ind 3 9735	Lang 82
Based on		Pcl 890.557			<b>ABB</b> ABB Network Partner AB		<b>IMRK 001 030-PA</b>
							Sheet 1
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